

In the Specification:

At page 1, the paragraph inserted before the "Technical Field" by the amendment dated February 5, 2002, has been replaced with the paragraph shown below:

RELATED PATENT DATA

This patent resulted from a continuation application of U.S. Patent Application Serial No. 09/228,705, filed January 12, 1999, now U.S. Patent No. 6,355,985 B1, issued March 12, 2002, entitled "Integrated Circuit Device, Synchronous-Link Dynamic Random Access Memory Device", naming Joseph M. Brand as inventor, which is a divisional application of U.S. Patent Number 6,008,074, issued December 28, 1999, entitled "Integrated Circuit Device, Synchronous-Link Dynamic Random Access Memory Device, Method of Forming An Integrated Circuit Device and Method of Forming a Synchronous-Link Dynamic Random Access Memory Edge-Mounted Device", naming Joseph M. Brand as inventor, the disclosure disclosures of which is are incorporated by reference.